



U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

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**INFORMATION DISCLOSURE
STATEMENT**

Docket Number
2885/16

Application Number
09/145,139

Filing Date
August 29, 1998

Examiner
T. Vo

Art Unit
2781

Invention Title
**INTERNAL BUS SYSTEM FOR DFPS AND UNITS
WITH TWO- OR MULTI-DIMENSIONAL
PROGRAMMABLE CELL ARCHITECTURES,
FOR MANAGING LARGE VOLUMES OF DATA
WITH A HIGH INTERCONNECTION
COMPLEXITY**

Inventor(s)
VORBACH et al.

Address to:
Assistant Commissioner for Patents
Washington, D.C. 20231

1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the following references to the attention of the Examiner. The references are listed on the attached modified PTO form 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
2. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated.
3. It is believed that no fees are due in connection with this Information Disclosure Statement. However, should any fees be due, the Commissioner is authorized to charge Deposit Account No. 11-0600 for such fees. A duplicate copy of this communication is enclosed for charging purposes.

Dated: 1/17/01

By: 

Andrew L. Reibman (Reg. No. P47893)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on

Date: 1/17/01 Atty's Reg. # P47,893

Atty's Signature 

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